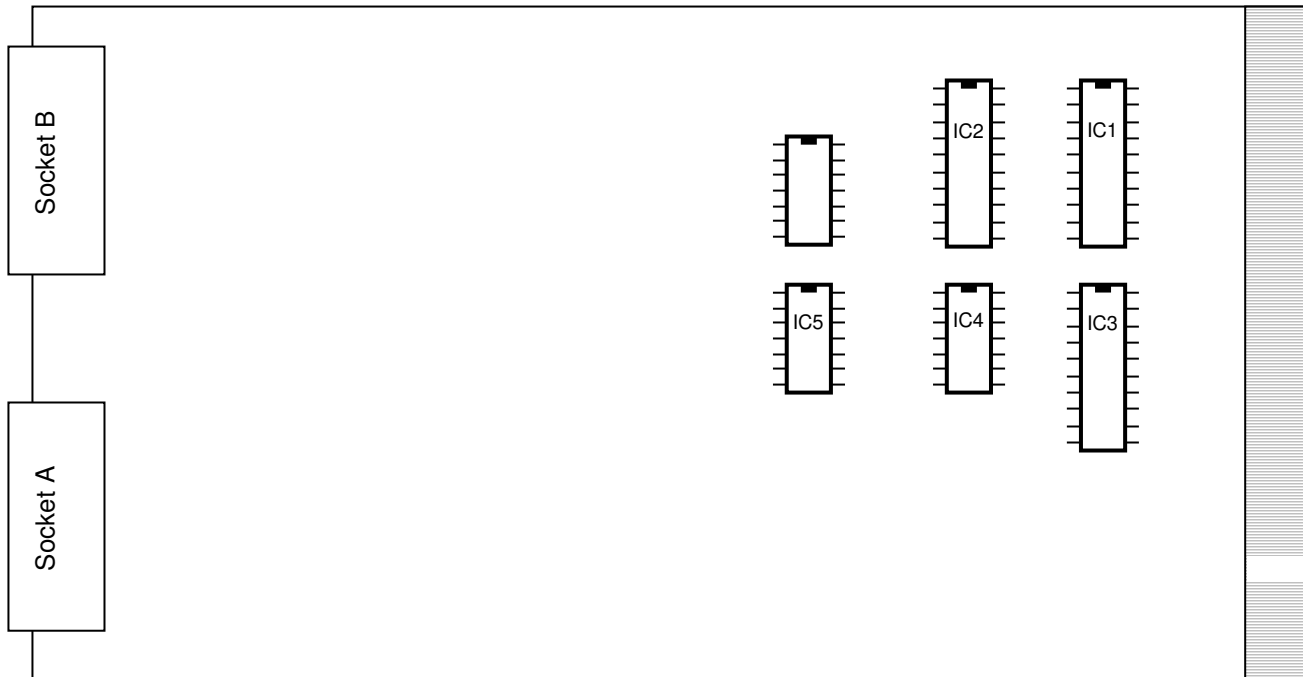


INTERAK COMPUTER SYSTEM

I/O Card v2

AP 2005

COMPONENT LAYOUT



This card will provide two blocks of 8 Ports.

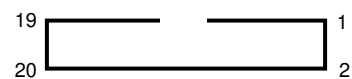
Socket A ... Ports 10 to 17 Hex

Socket B ... Ports 18 to 1F Hex

These can be further decoded as required
using A0 to A2.

IC2 provides buffering for signals from the edge
connector and is permanently enabled

20 way IDC (socket A + B)



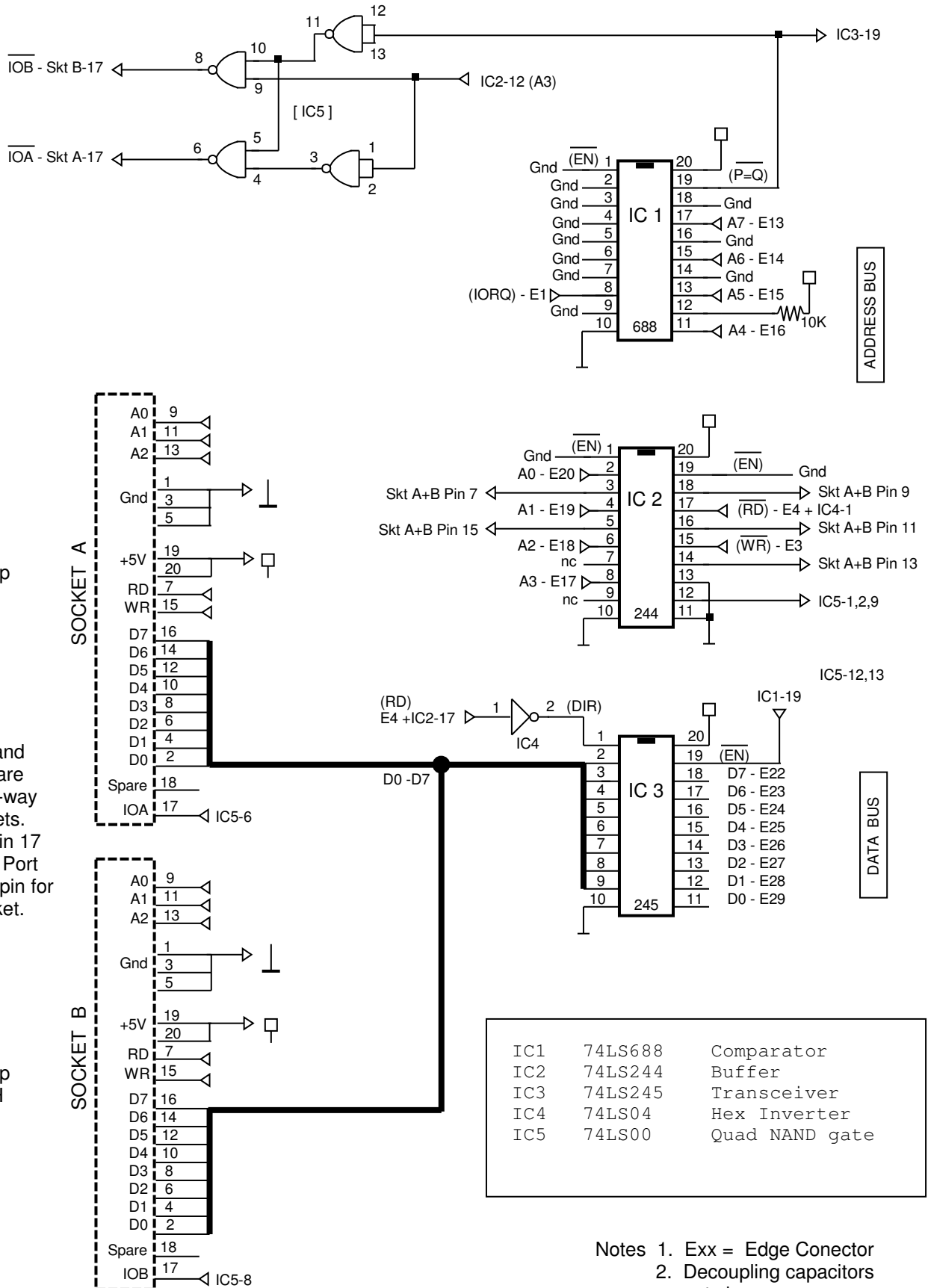
PIN ALLOCATION

1	Gnd	2	D0
3	Gnd	4	D1
5	Gnd	6	D2
7	RD	8	D3
9	A0	10	D4
11	A1	12	D5
13	A2	14	D6
15	WR	16	D7
17	IOA + IOB	18	Spare
19	+5V	20	+5V

INTERAK COMPUTER SYSTEM

I/O Card v2

AP 2008



Skt A
Port Group
10 - 17H

Socket A and
Socket B are
identical 20-way
IDC sockets.
Except for Pin 17
which is the Port
Group select pin for
each socket.

Skt B
Port Group
18 - 1FH

IC1	74LS688	Comparator
IC2	74LS244	Buffer
IC3	74LS245	Transceiver
IC4	74LS04	Hex Inverter
IC5	74LS00	Quad NAND gate

Notes 1. Exx = Edge Conector
2. Decoupling capacitors
not shown.